

FIG.1 PRIOR ART

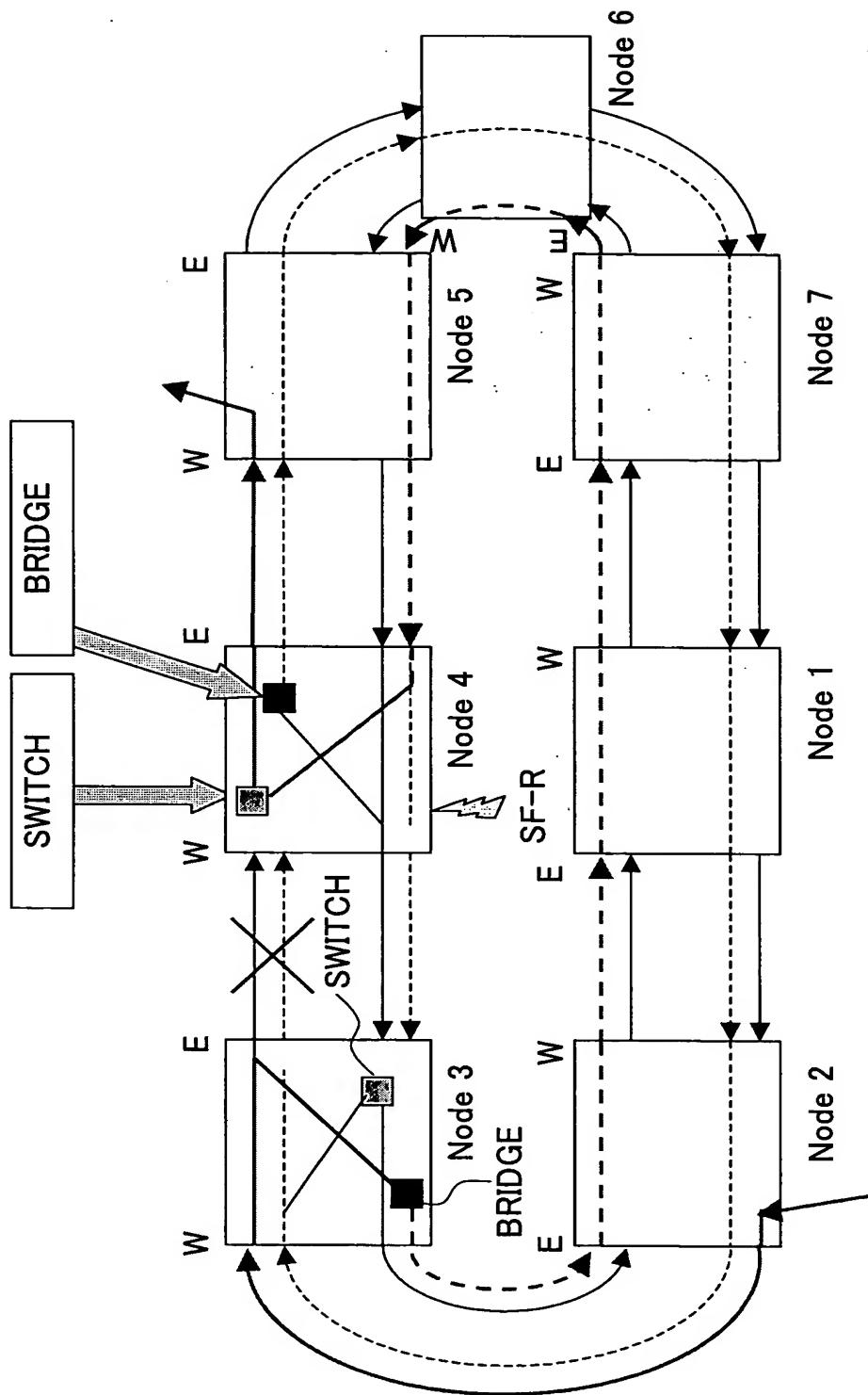


FIG.2 PRIOR ART

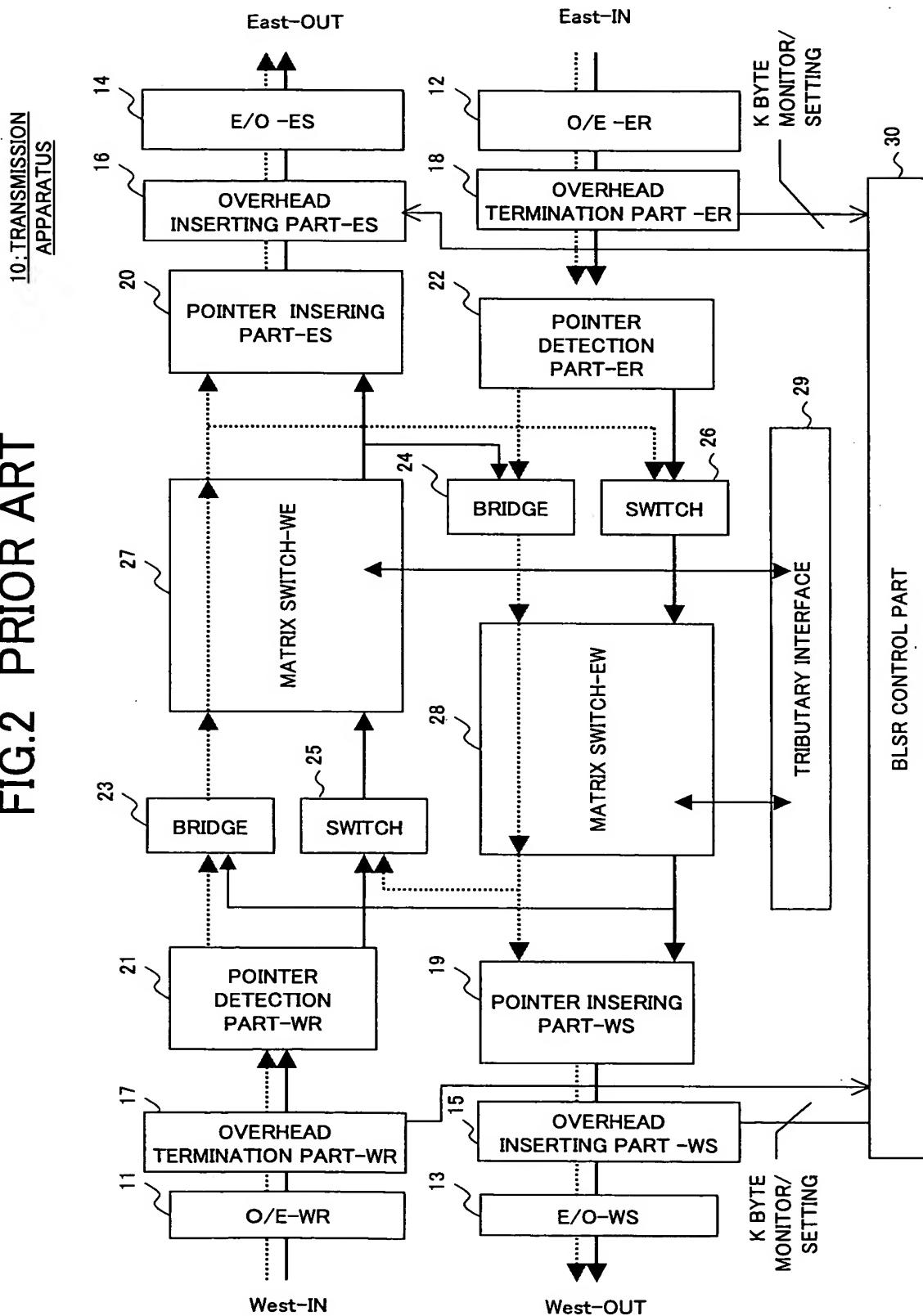
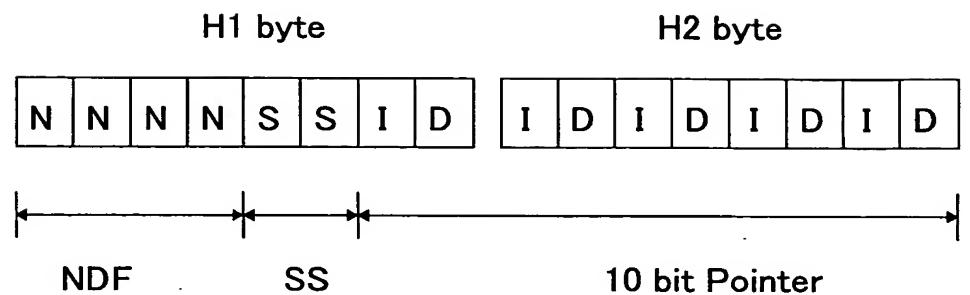


FIG.3 PRIOR ART



I : Increment Indication
D : Decrement Indication
N : New Data Flag
S : SS bits

FIG.4 PRIOR ART

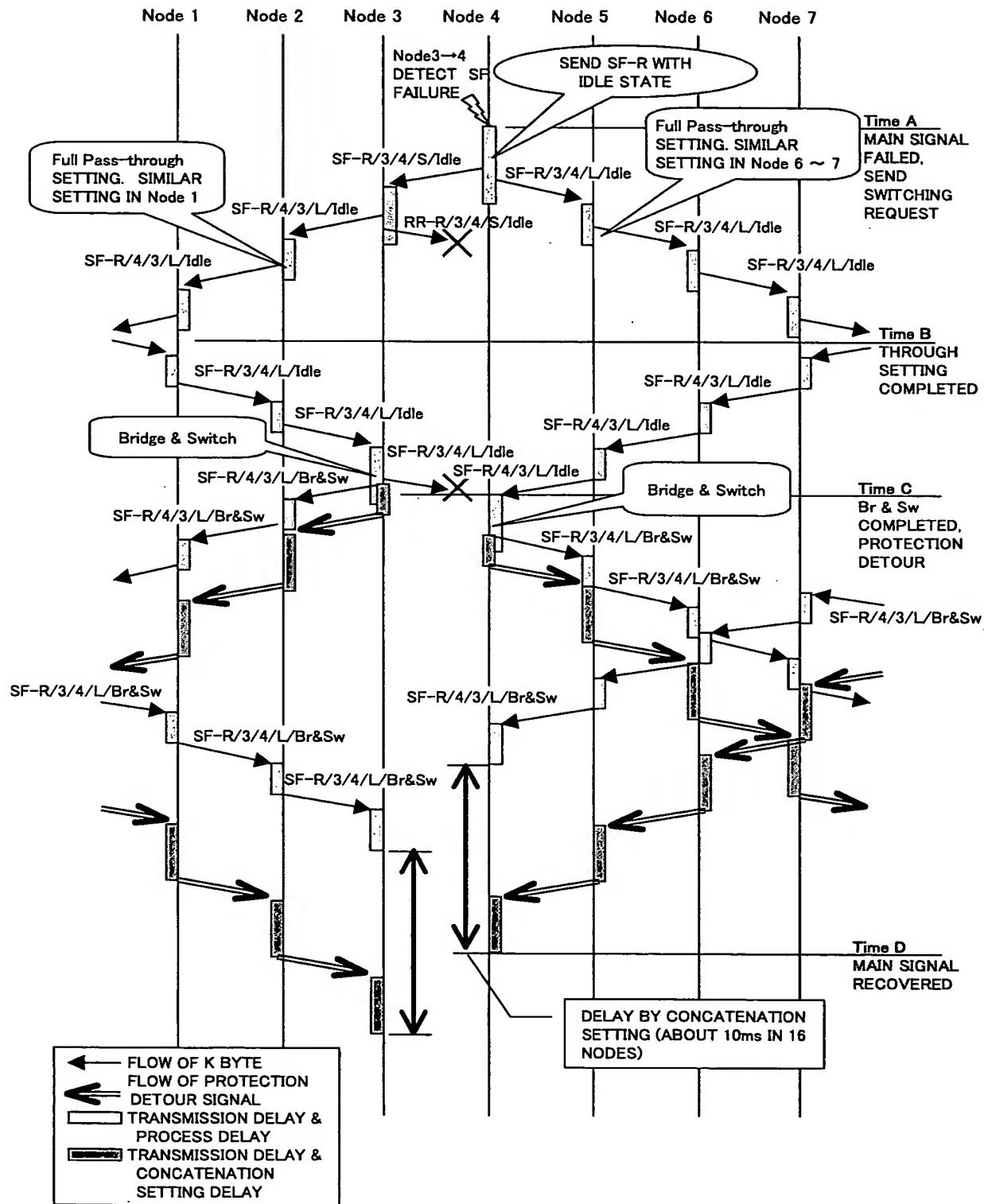


FIG.5A PRIOR ART

K1 Byte				K1 Byte			
Ring Bridge Request		Destination Node ID		Source Node ID		L/S	Status
1	2	3	4	5	6	7	8
1	2	3	4	5	6	7	8
1	2	3	4	1	2	3	4
1	2	3	4	5	6	7	8

FIG.5B PRIOR ART

LONG PATH				SHORT PATH			
SF-R		3		4		L	IDLE
SF-R		3		4		L	IDLE
SF-R		3		4		S	IDLE

FIG.6

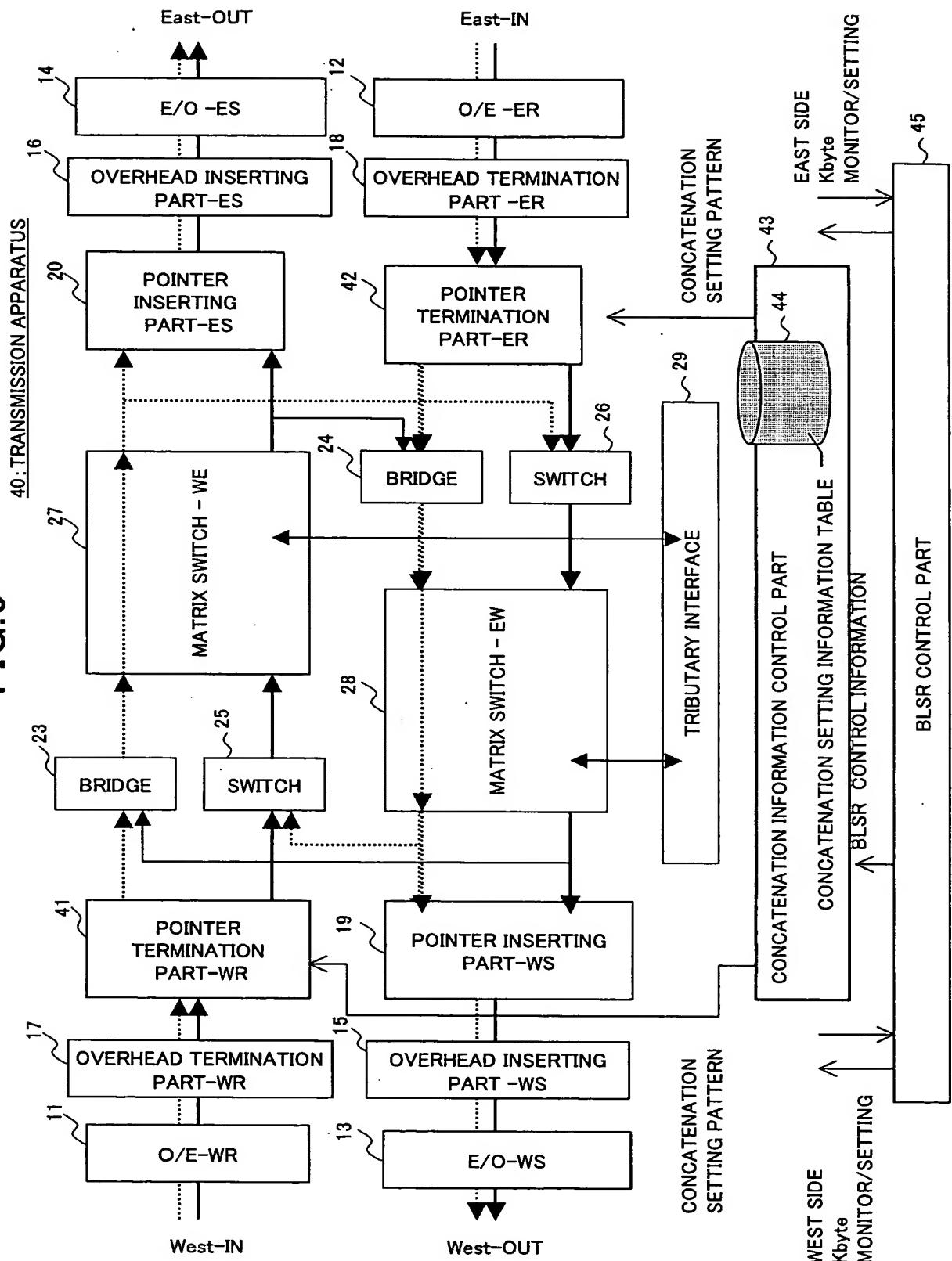


FIG. 7

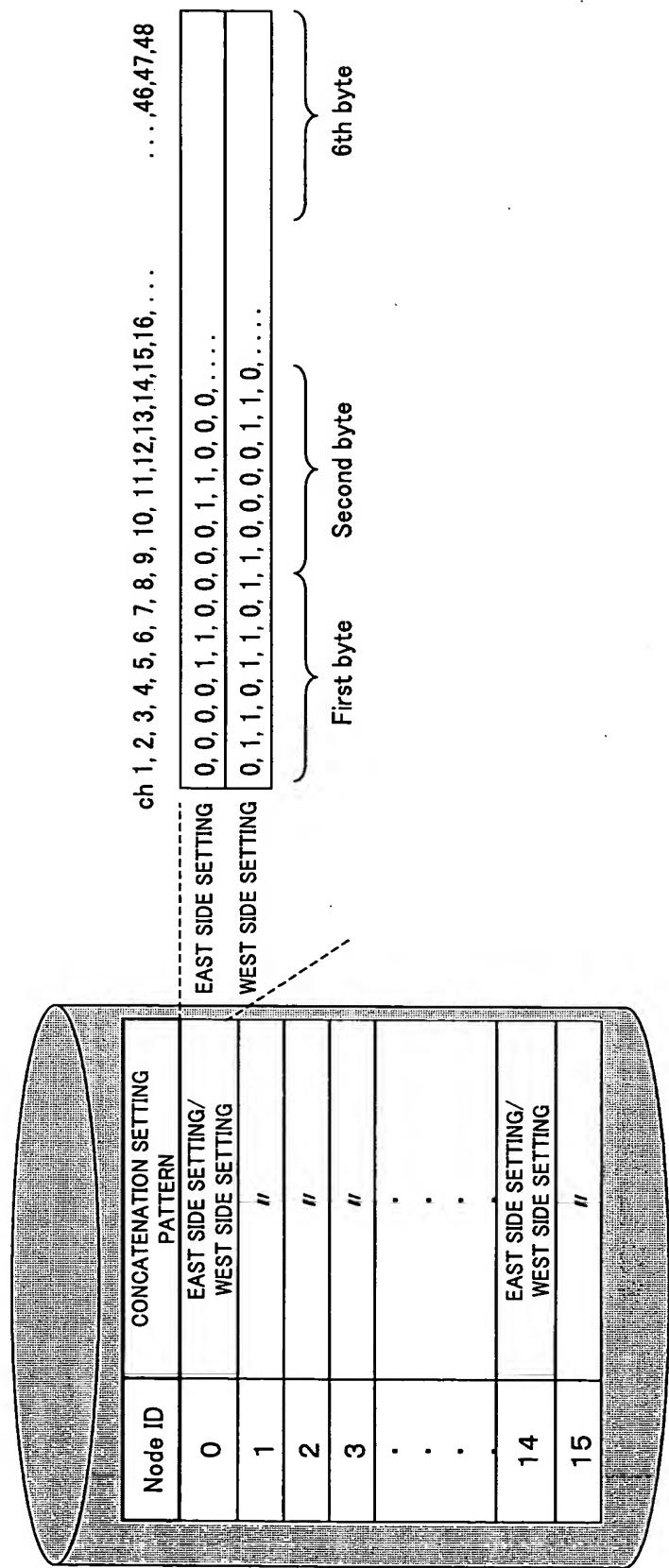


FIG.8

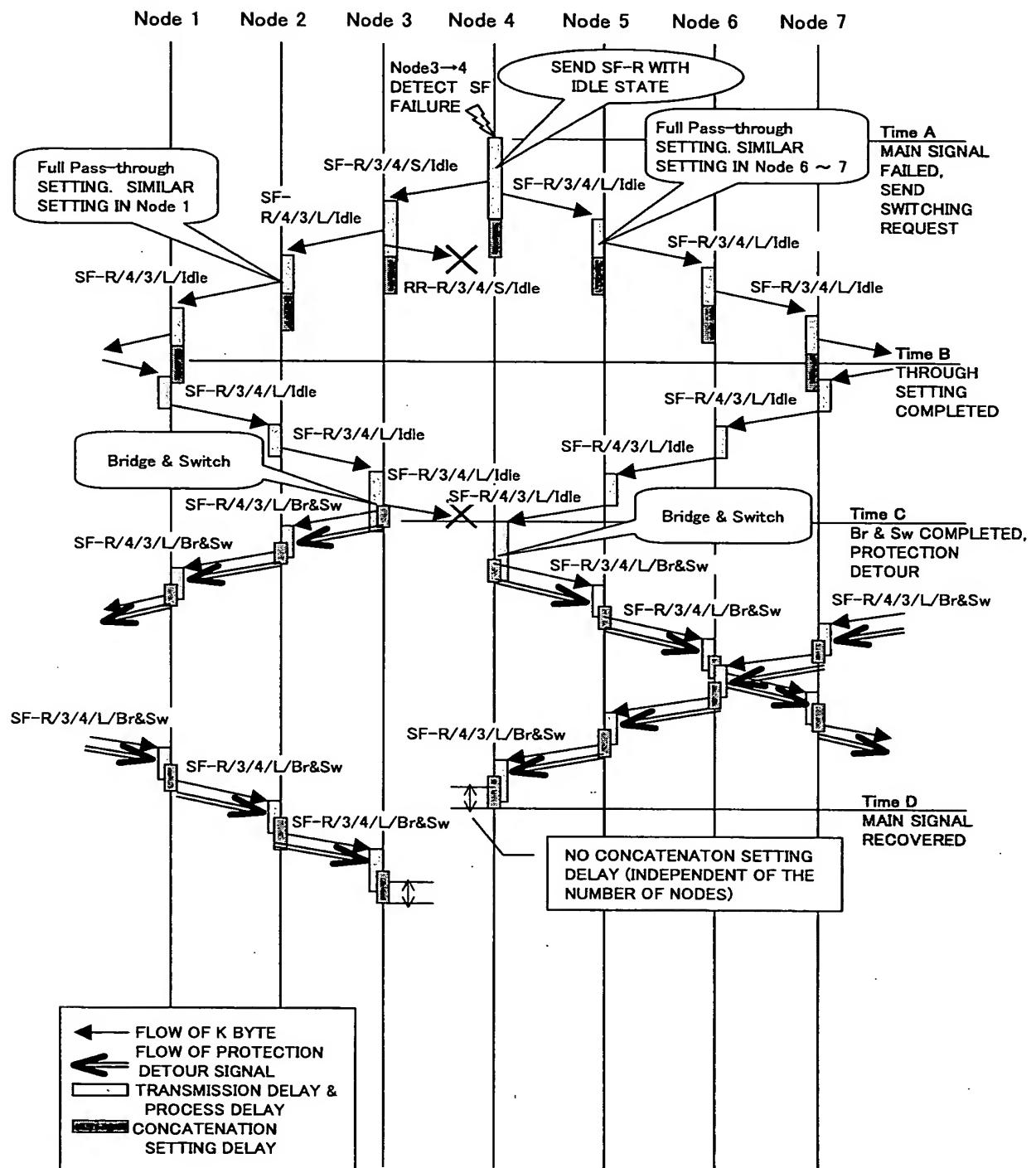


FIG.9

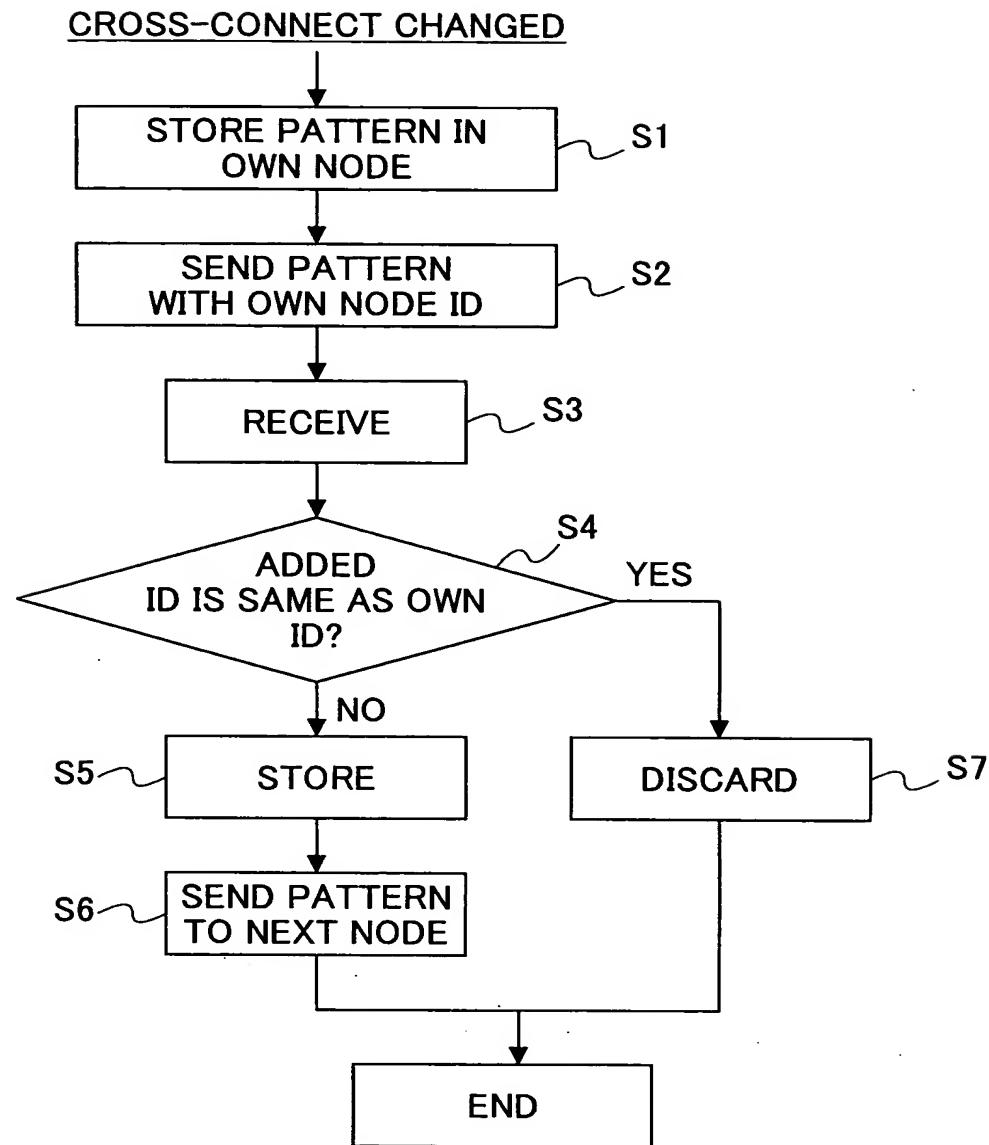


FIG.10

AU4 in STM1

RSOH	A1	A1	A1	A2	A2	A2	J0	Z0	Z0
	B1			E1			F1		
	D1			D2			D3		
AU Pointer	H1	H1*	H1*	H2	H2*	H2*	H3	H3	H3
	B2	B2	B2	K1			K2		
	D4			D5			D6		
MSOH	D7			D8			D9		
	D1			D1			D1		
	0			1			2		
S1	Z1	Z1	Z2	Z2	M1	E2			

 Undefind Overhead Byte

* Concatenation Indication
H1*=1001XX11
H2*=11111111

FIG.11

